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042

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Fig.1

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PRIOR ART

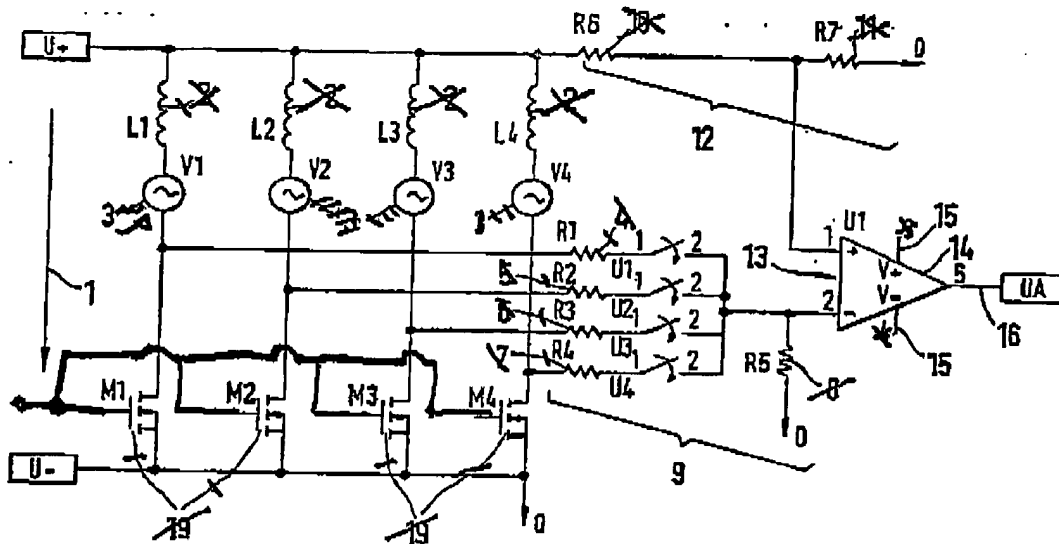
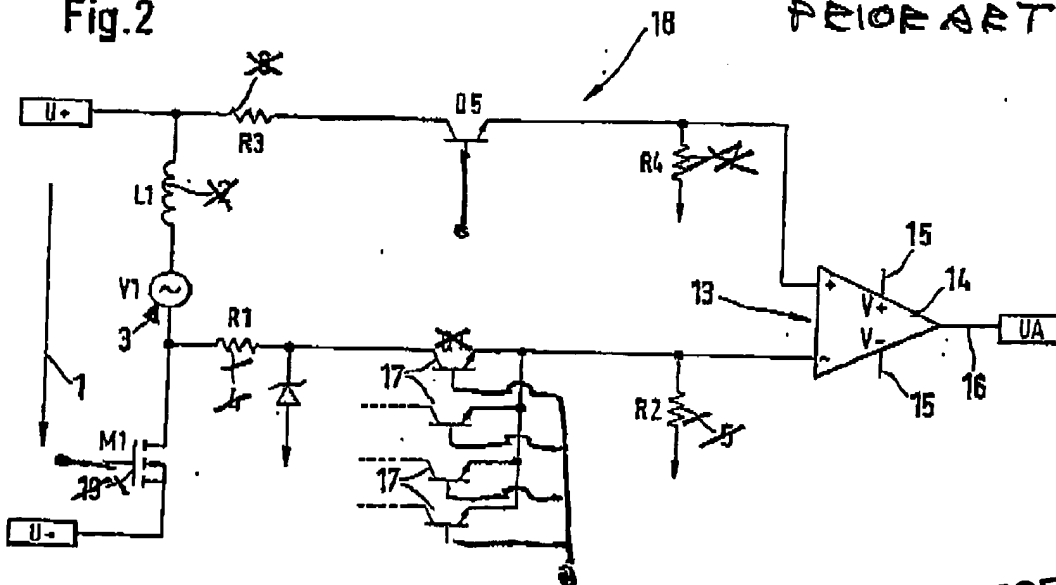


Fig.2

PRIOR ART



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PRIOR ART

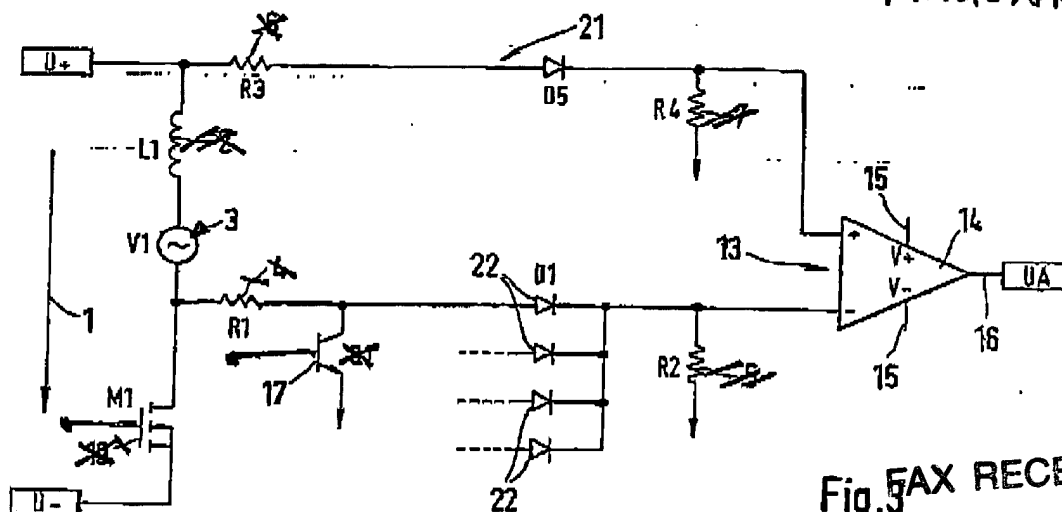


Fig. 3

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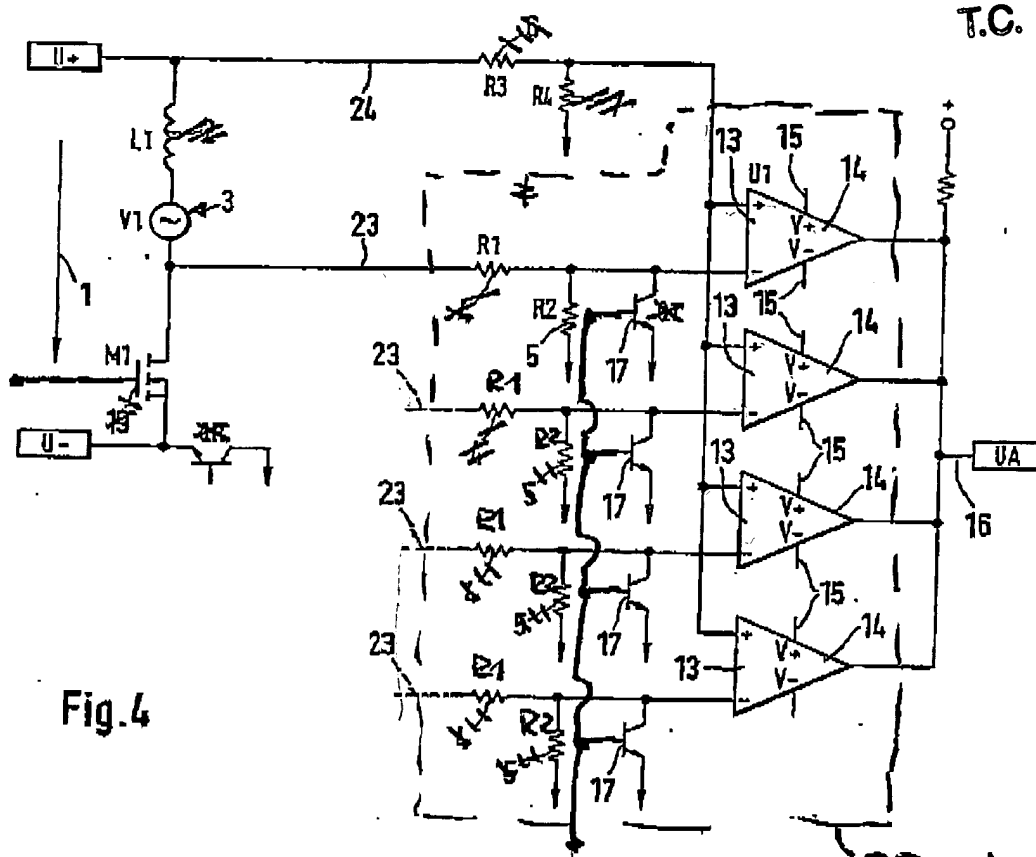


Fig. 4

OR circuit